

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	("4797579").PN.	USPAT	OR	OFF	2005/09/28 08:00
L2	5	((("4829199") or ("5041741") or ("5598348") or ("5625803") or ("5768145")).PN.	USPAT	OR	OFF	2005/09/28 08:09
L3	166	"726"/\$.ccls. and CMOS	USPAT	OR	OFF	2005/09/28 08:10
L4	85	"726"/\$.ccls. and "713"/\$.ccls. and CMOS	USPAT	OR	OFF	2005/09/28 08:27
L5	10	713/172.ccls. and CMOS	USPAT	OR	OFF	2005/09/28 09:08
L6	6	713/192.ccls. and CMOS	USPAT	OR	OFF	2005/09/28 08:28
L7	25	713/194.ccls. and CMOS	USPAT	OR	OFF	2005/09/28 09:06
L8	87	326/31.ccls. and CMOS	USPAT	OR	OFF	2005/09/28 08:39
L9	3	713/174.ccls. and CMOS	USPAT	OR	OFF	2005/09/28 09:07
L11	417	708/250-252.ccls.	USPAT	OR	OFF	2005/09/28 09:13
L12	97	708/254-256.ccls.	USPAT	OR	OFF	2005/09/28 09:13
L13	1222	713/1.ccls.	USPAT	OR	OFF	2005/09/28 09:13
L14	246	713/172.ccls.	USPAT	OR	OFF	2005/09/28 09:13
L15	51	713/174.ccls.	USPAT	OR	OFF	2005/09/28 09:13
L16	280	713/185.ccls.	USPAT	OR	OFF	2005/09/28 09:14
L17	819	713/191-194.ccls.	USPAT	OR	OFF	2005/09/28 09:14
L18	73	235/462.26	USPAT	OR	OFF	2005/09/28 09:14
L19	51	235/462.28	USPAT	OR	OFF	2005/09/28 09:14
L20	3320	235/487	USPAT	OR	OFF	2005/09/28 09:14
L21	4242	235/492	USPAT	OR	OFF	2005/09/28 09:14
L22	50	326/8.ccls.	USPAT	OR	OFF	2005/09/28 09:14
L23	744	326/21-22.ccls.	USPAT	OR	OFF	2005/09/28 09:15
L24	320	326/26.ccls.	USPAT	OR	OFF	2005/09/28 09:15
L25	76	326/28.ccls.	USPAT	OR	OFF	2005/09/28 09:15
L26	85	326/29.ccls.	USPAT	OR	OFF	2005/09/28 09:15
L27	202	326/31.ccls.	USPAT	OR	OFF	2005/09/28 09:15
L28	527	326/62-63.ccls.	USPAT	OR	OFF	2005/09/28 09:15
L29	709	326/81.ccls.	USPAT	OR	OFF	2005/09/28 09:15
L30	178	326/108.ccls.	USPAT	OR	OFF	2005/09/28 09:15
L31	720	326/121.ccls.	USPAT	OR	OFF	2005/09/28 09:16
L32	16497	(CMOS or integrated adj2 circuit or IC or tamper adj2 resistant) near5 (config\$9)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/28 09:18
L33	16906	(CMOS or integrated adj2 circuit or IC or tamper adj2 resistant or smart adj2 card) near5 (config\$9)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/28 09:19
L34	442	(CMOS or integrated adj2 circuit or IC or tamper adj2 resistant or smart adj2 card) near5 (config\$9) same (edge)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/28 09:19
L35	1704	(CMOS or integrated adj2 circuit or IC or tamper adj2 resistant or smart adj2 card) near5 (config\$9) same (switch\$4)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/28 09:20
L36	97	(CMOS or integrated adj2 circuit or IC or tamper adj2 resistant or smart adj2 card) near5 (config\$9) same (switch\$4) same delay\$5	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/28 09:21

L37	8	I13 and I35	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/28 09:21
L38	8	I20 and I35	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/28 09:21
L39	4	I17 and I35	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/28 09:22
L40	37243	rising adj2 edge or falling adj2 edge	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/28 09:22
L41	230	I35 and I40	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/28 09:23
L42	6971	pulse near4 (generat\$4 or creat\$4 or deriv\$4 or produc\$4) same I40	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/28 09:25
L44	361810	pulse near4 (generat\$4 or creat\$4 or deriv\$4 or produc\$4)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/28 09:25
L45	16045	I40 and I44	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/28 09:25
L46	49	I42 and I35	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/28 09:28
L47	439	(pseudorandom\$4 or random\$4) and I35	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/28 09:31
L48	175	delay\$4 near6 (switch\$4) and I35	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/28 09:34
L49	47	708/675.ccls.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/28 09:35
L50	123	708/817-818.ccls.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/28 09:36
L51	141	708/820-821.ccls.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/28 09:37

SYSTEM:OS - DIALOG OneSearch

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File 696:DIALOG Telecom. Newsletters 1995-2005/Sep 27  
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**\*File 696: New data from Espicom added; see HELP NEWS 696.**

Set	Items	Description
?	s	(IC or CMOS or tamperproof or tamper (2n) proof or integrated (2n) circuit or smart (2n) card?)
>>>	File 34	processing for CARD? stopped at CARDISSA
>>>	File 144	processing for CARD? stopped at CARDIOTHERMOGRAPHIE
Processed 10 of 20 files ...		
Processing		
Completed processing all files		
	236963	IC
	203635	CMOS
	549	TAMPERPROOF
	10956	TAMPER
	431512	PROOF
	2239	TAMPER(2N)PROOF
	2212425	INTEGRATED

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1343633 CIRCUIT
308561 INTEGRATED(2N)CIRCUIT
281782 SMART
2152399 CARD?
48667 SMART(2N)CARD?
S1 685116 (IC OR CMOS OR TAMPERPROOF OR TAMPER (2N) PROOF OR
INTEGRATED (2N) CIRCUIT OR SMART (2N) CARD?)
? s (configuration or configured or configures) (s) s1
867008 CONFIGURATION
108830 CONFIGURED
5640 CONFIGURES
685116 S1
S2 8426 (CONFIGURATION OR CONFIGURED OR CONFIGURES) (S) S1
? s (switch? and delay?) (s) s2
1249318 SWITCH?
1128575 DELAY?
8426 S2
S3 114 (SWITCH? AND DELAY?) (S) S2
? s (pulse?) (3n) (generat? or creat? or produce or produces or producing or
deriv?)
Processing
Processed 10 of 20 files ...
Completed processing all files
1270038 PULSE?
4597456 GENERAT?
3098307 CREAT?
1297463 PRODUCE
537180 PRODUCES
706456 PRODUCING
4147413 DERIV?
S4 109379 (PULSE?) (3N) (GENERAT? OR CREAT? OR PRODUCE OR PRODUCES
OR PRODUCING OR DERIV?)
? s (rising (2n) edge?) or (falling (2n) edge?)
293189 RISING
1123510 EDGE?
1524 RISING(2N)EDGE?
177749 FALLING
1123510 EDGE?
982 FALLING(2N)EDGE?
S5 1937 (RISING (2N) EDGE?) OR (FALLING (2N) EDGE?)
? s (random? or pseudorandom?)
1608113 RANDOM?
13456 PSEUDORANDOM?
S6 1615680 (RANDOM? OR PSEUDORANDOM?)
? s s1 and s6
685116 S1
1615680 S6
S7 24681 S1 AND S6
? s s7 and s3
24681 S7
114 S3
S8 9 S7 AND S3
? s s5 and s6
1937 S5
1615680 S6
S9 117 S5 AND S6
? s s5 and s4
1937 S5
109379 S4
S10 135 S5 AND S4

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? s delay? or (2n) (switch?)
>>>Operator "(2N)" in invalid position
? s delay? (2n) (switch?)
      1128575 DELAY?
      1249318 SWITCH?
      S11      5627 DELAY? (2N) (SWITCH?)
? s s5 and s11
      1937 S5
      5627 S11
      S12      2 S5 AND S11
? s s6 and s11
      1615680 S6
      5627 S11
      S13      229 S6 AND S11
? s s13 and s1
      229 S13
      685116 S1
      S14      28 S13 AND S1
```